Fig. 1

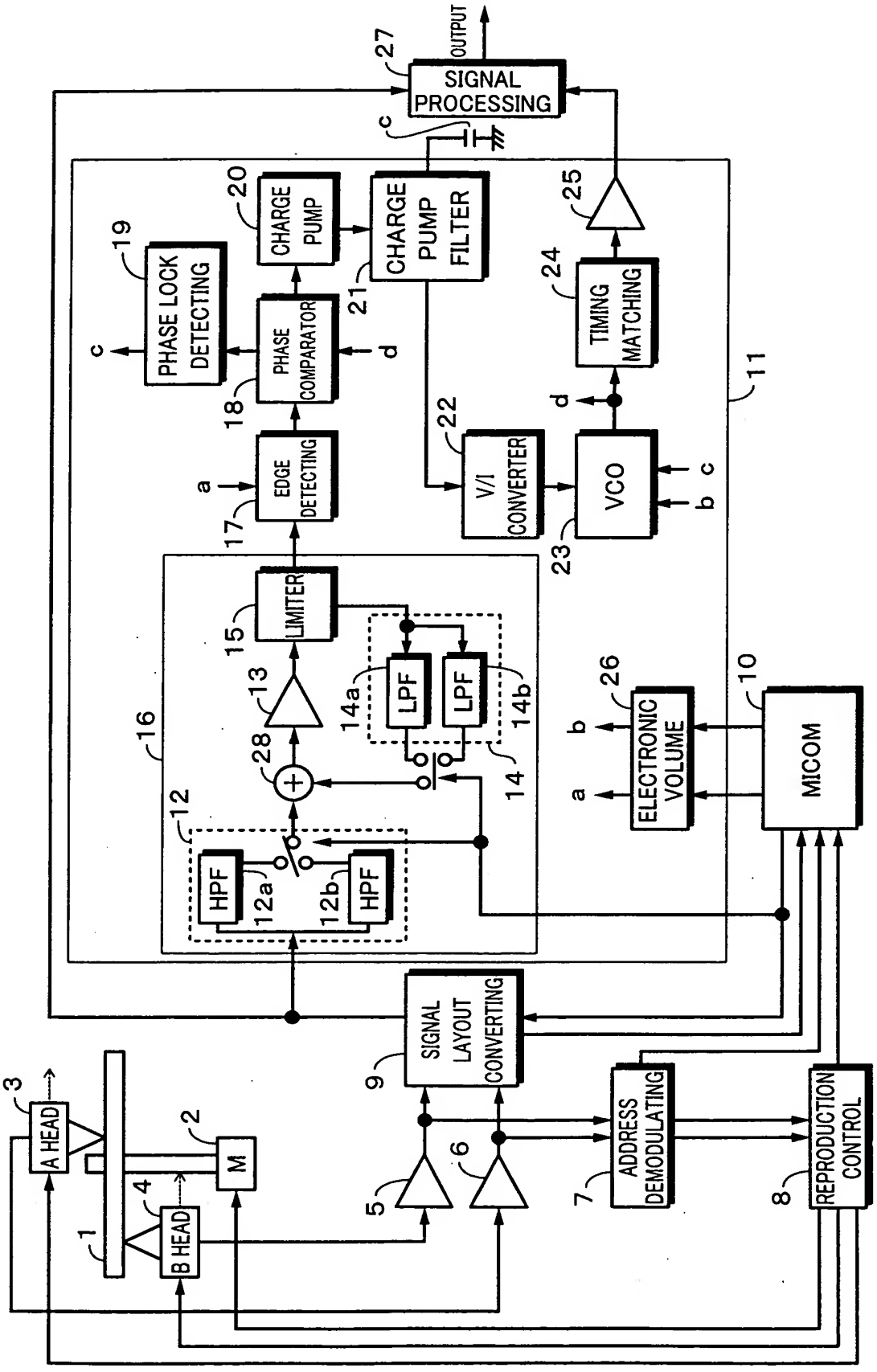
SIGNAL PROCESSING
OUTPUT
27
c
CHARGE PUMP
20
CHARGE PUMP FILTER
21
25
PHASE LOCK DETECTING
19
PHASE COMPARATOR
18
TIMING MATCHING
24
EDGE DETECTING
17
V/I CONVERTER
22
VCO
23
11
LIMITER
15
LPF
LPF
14a
14b
13
16
28
14
26
ELECTRONIC VOLUME
10
MICOM
12
HPF
12a
12b
HPF
SIGNAL LAYOUT CONVERTING
9
3
A HEAD
2
M
4
B HEAD
1
5
6
7
ADDRESS DEMODULATING
8
REPRODUCTION CONTROL

Fig. 2A

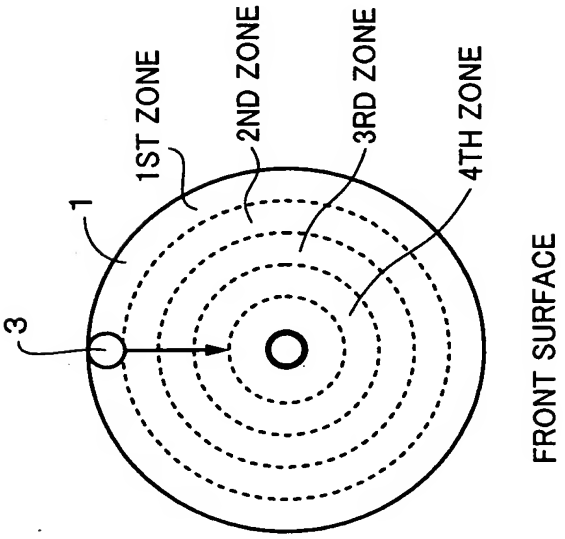
1
1ST ZONE
2ND ZONE
3RD ZONE
4TH ZONE
3
FRONT SURFACE

Fig. 2B

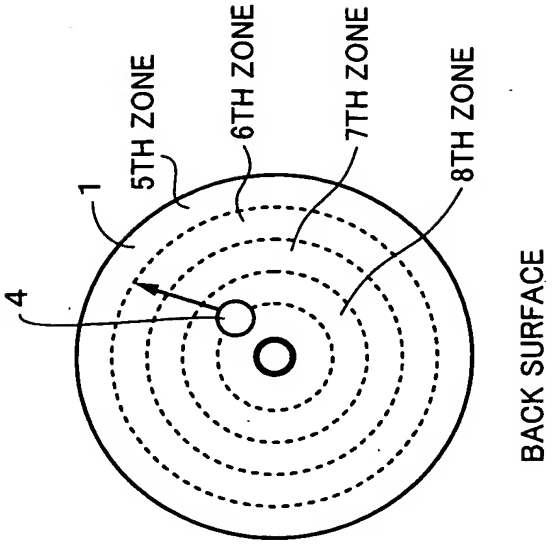
1
5TH ZONE
6TH ZONE
7TH ZONE
8TH ZONE
4
BACK SURFACE

Fig. 3A

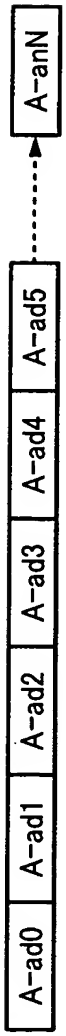

Fig. 3B

Fig. 3C

Fig. 3D

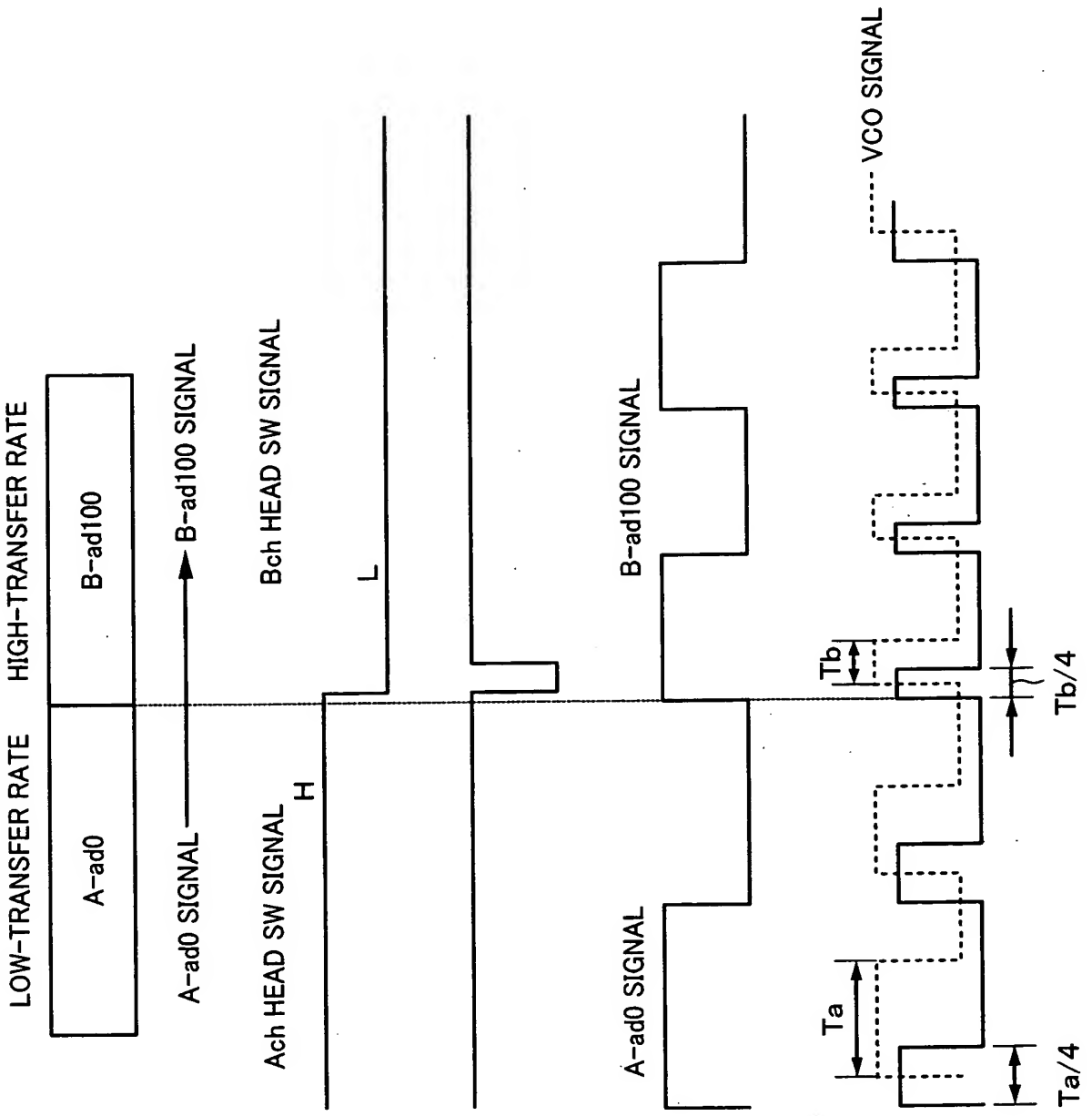

Fig. 4A

Fig. 4B

Fig. 4C

Fig. 4D

Fig. 4E

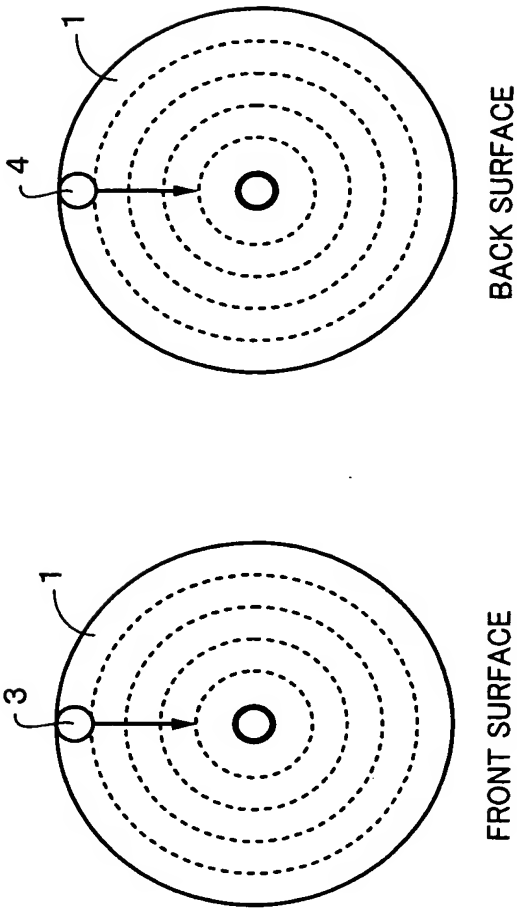
1
4
BACK SURFACE
1
3
FRONT SURFACE

Fig. 5A

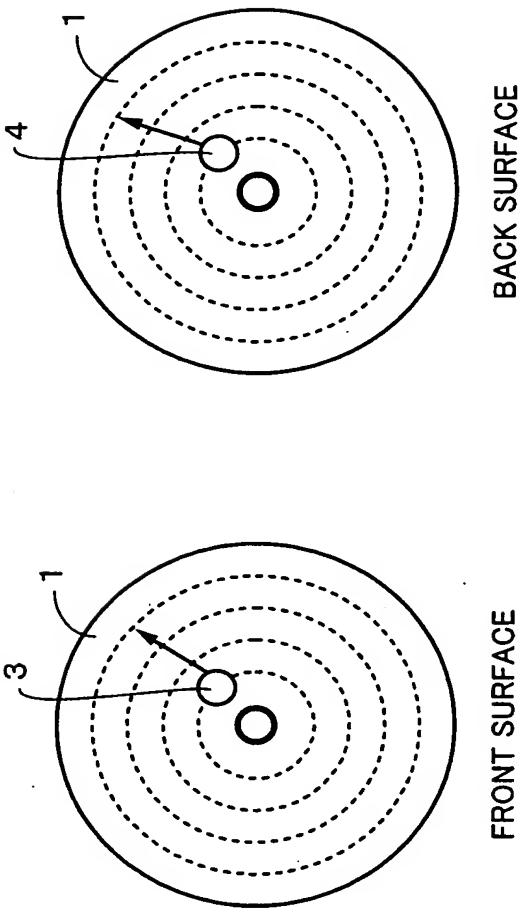
1
4
BACK SURFACE
1
3
FRONT SURFACE

Fig. 5B

DESCRIPTION OF REFERENCE NUMERALS

1	DISC-SHAPED RECORDING MEDIUM
2	SPINDLE MOTOR
3	Ach HEAD
4	Bch HEAD
5, 6	PREAMPLIFIER
7	ADDRESS DEMODULATING UNIT
8	REPRODUCTION CONTROL UNIT
9	SIGNAL LAYOUT CONVERTING CIRCUIT
10	MICROCOMPUTER
11	CLOCK EXTRACTING UNIT
16	WAVEFORM EQUALIZING CIRCUIT
17	EDGE DETECTING CIRCUIT
18	PHASE COMPARATOR
19	PHASE LOCK DETECTING CIRCUIT
20	CHARGE PUMP
21	CHARGE PUMP FILTER
23	VOLTAGE CONTROLLED OSCILLATOR
26	ELECTRONIC VOLUME
27	SIGNAL PROCESSING UNIT